

FORM PTO-1449 (SUBSTITUTE)

FEB 15 2002

Sheet 1 of 2

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(37 CFR 1.98(b))Attorney Docket No.: L&L-I0178
Applic. No. 09/996,279

Applicant

Lothar Risch et al.

Filing Date November 28, 2001
Group Art UnitCOPY OF PAPERS
ORIGINALLY FILED

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
<i>SL</i>	A	5,120,666	06/09/92	Gotou			
<i>SL</i>	B	5,646,058	07/08/97	Taur et al.			
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES	NO
<i>SL</i>	J	44 33 086 C2	03/30/95	Germany				X
	K	198 03 479 A1	12/24/98	Germany				X
	L	0 612 103 A2	08/24/94	Europe			X	
<i>SL</i>	M	0 704 909 A2	04/03/96	Europe			X	
	N							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

<i>SL</i>	O	Colinge, J.P. et al.: "Silicon-On-Insulator 'Gate-All-Around Device'", IEDM 1990, pp. 595-598
<i>SL</i>	P	Wong, Hon-Sum Philip et al.: "Self-Aligned (Top and Bottom) Double-Gate MOSFET with a 25 nm Thick Silicon Channel", IEDM 1997, pp. 427-430

EXAMINER *SL* DATE CONSIDERED *7/29/02*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609;
Draw line through citation if not in conformance and not considered. Include copy of this form with
next communication to applicant.RECEIVED
APR 16 2002
10:28 AM
MAIL ROOM

#5

FEB 15 2002

Sheet 2 of 2

FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(37 CFR 1.98(b))Attorney Docket No.: L&L-I0178 Appl. No.
09/996,279

Applicant

Lothar Risch et al.

Filing Date Group Art Unit
November 28, 2001COPY OF PAPERS
ORIGINALLY FILED

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
A						
B						
C						
D						
E						
F						
G						
H						
I						

FOREIGN PATENT DOCUMENT

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES/NO
J						
K						
L						
M						
N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

<i>R</i>	O	Pikus, F.G. et al.: "Nanoscale field-effect transistors: An ultimate size analysis", Appl. Phys. Lett. 1971, Vol. 25, December 22, 1997, pp. 3661-3663
	P	
EXAMINER	<i>R</i>	DATE CONSIDERED <i>26/04/01</i>

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609;
Draw line through citation if not in conformance and not considered. Include copy of this form with
next communication to applicant.

RECEIVED
APR 16 2002
10:25:00 AM
ROOM